FIG. 1A

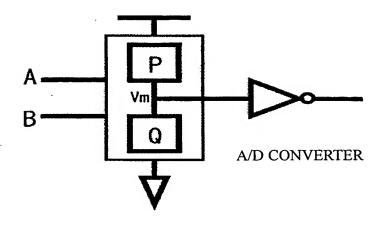


FIG. 1B

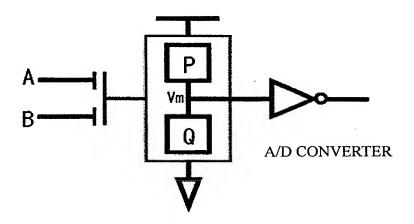


FIG. 2A

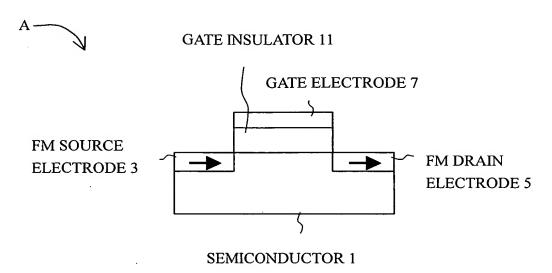
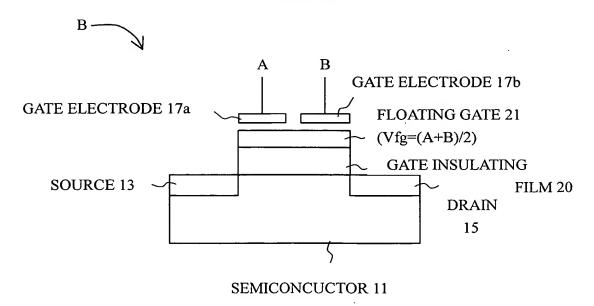


FIG. 2B



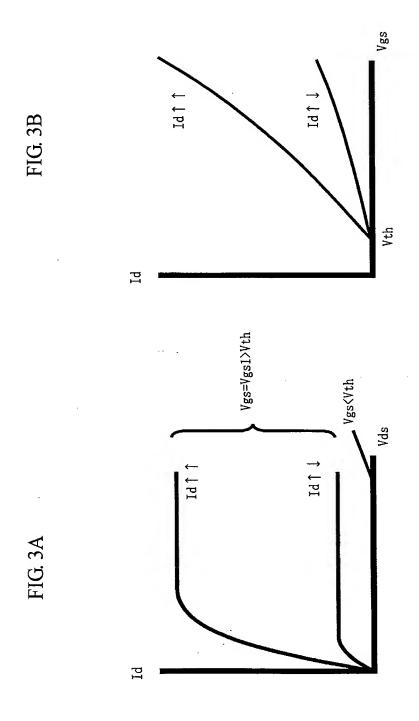


FIG. 4

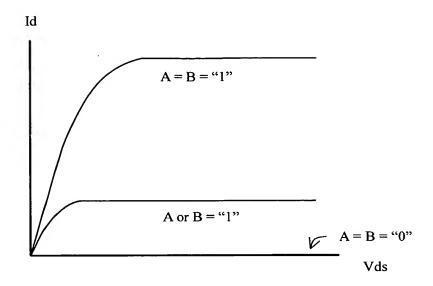


FIG. 5A

FIG. 5B

FIG. 5C

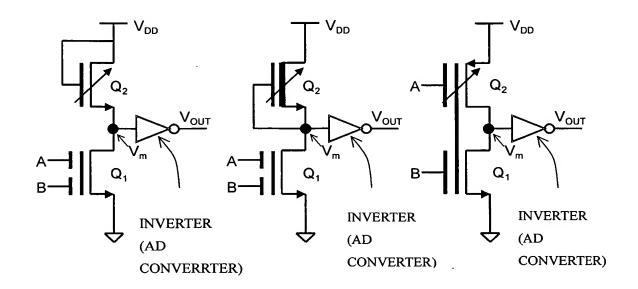


FIG. 6

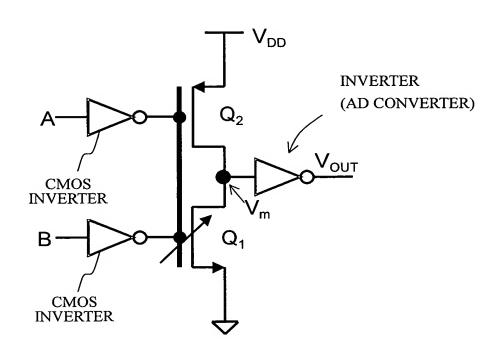


FIG. 7

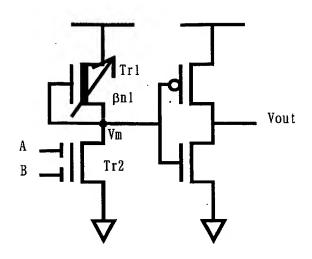


FIG. 8

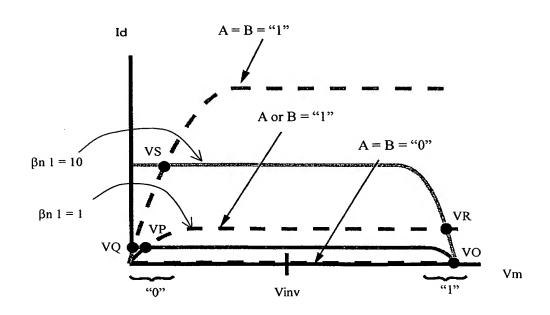


FIG. 9

(A)					(B)				
$\beta n1 = 1$					$\beta$ n 1 = 10				
	A	В	Vm	Vout	A	В	Vm	Vout	
	0	0	VO	"0"	0	0	VO	"0"	
	0	1	VP	"1"	0	1	VR	"0"	
	1	1	VQ	"1"	1	1	VS	"1"	
OR				1	AND				

FIG. 10

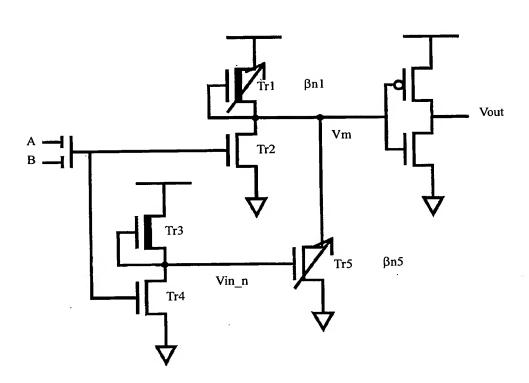


FIG. 11

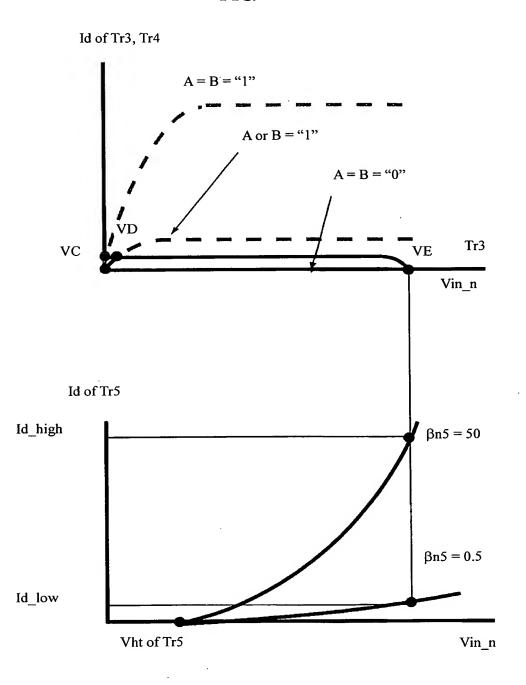


FIG. 13B

A B Vm Vout
0 0 VT 0
0 1 VR 0
1 I VŠ

XNOR

 $\beta n5 = 50$ ,  $\beta n1 = 10$ 

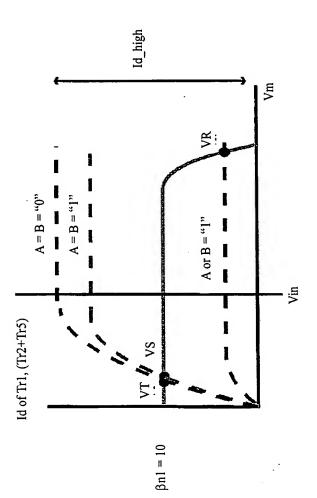
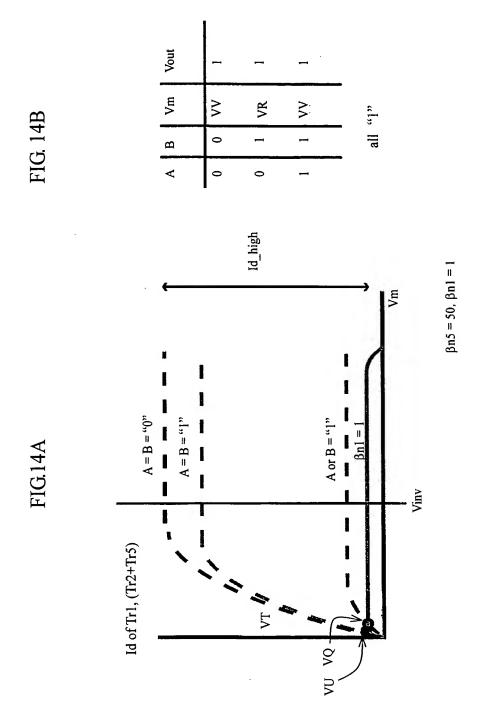


FIG. 13A



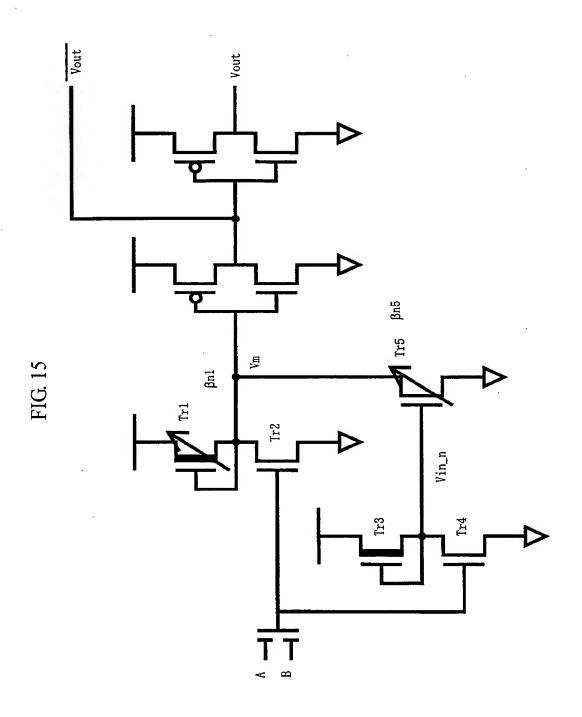


FIG. 16B

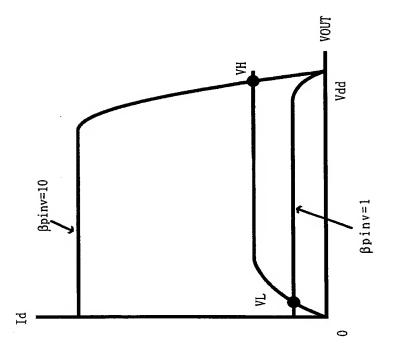


FIG. 16A

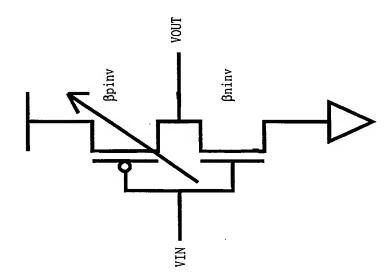


FIG. 17

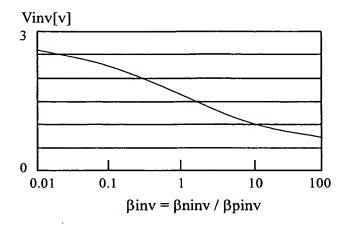
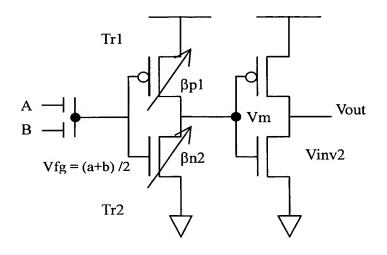


FIG. 18



Vm Vout FIG. 19B δ OR В  $\beta p1 = 1$ ,  $\beta n2 = 10$ A or B = "[]" A = B = 0Vinv2 FIG. 19A рĮ

0/

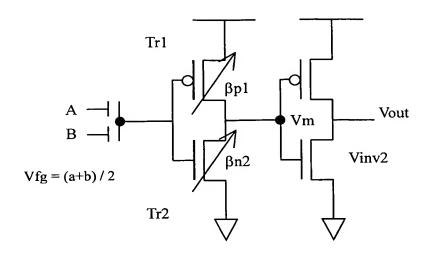
Vout	.,0,,	0,,	"I"	
Λm	۸O	VR	λQ	
Ф	0	-	-	
A	0	0	-	

Id A = B = "1"  $A \circ r B = "1"$  A = B = "0" A = B = "0" A = B = "0" A = B = "1" A = B = "1"

 $\beta p1 = 10$ ,  $\beta n2 = 1$ 

AND

FIG. 21



<u>m</u>		Vm	0>	VP	۸ م	- OR			
FIG. 22B		В	0	_	_	_			
FIC		A	0	0	_				
						Vfg			: 10)
					OA	1			$Vinv1_low (\beta p1 = 1, \beta n2 = 10)$
					>		1	_	βp1 = 1
									1_low (
									Vinv
					VP		0		
2A						Vinv1_low			
FIG. 22A						Vinv			
迁									
	Λm	0A					0	0	ı
		>	"1"	Vinv2	0,,		¥	В	

OR

Vm Vout 0/ FIG. 23B AND В 0 Vinv1\_low  $(\beta p1 = 10, \beta n2 = 1)$ ٥ Vinv1\_high FIG. 23A Vm ΛO Vinv2 "I" .0,

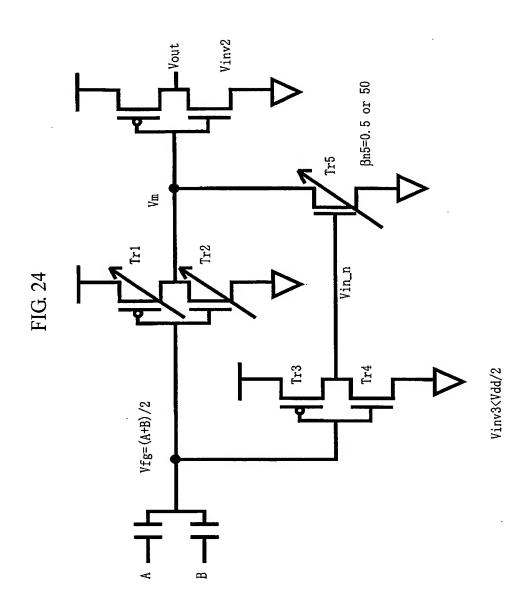


FIG. 25

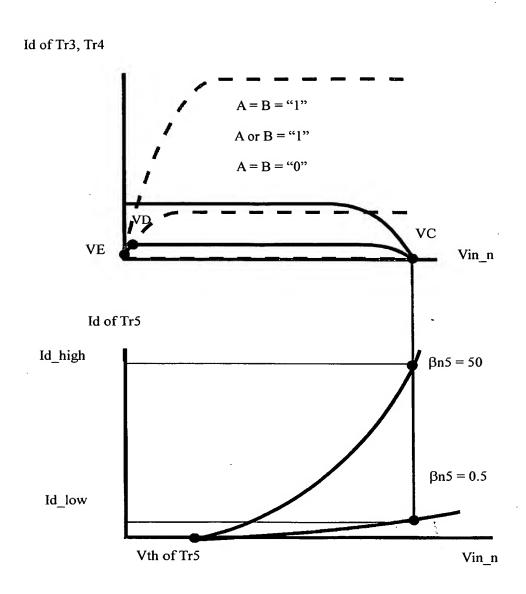


FIG. 26A FIG. 26B

Id of Tr1, (Tr2 + Tr5)

Vm Vout "]" ,,0,, 0/ δ OR 0 ٧  $\beta p1 = 1$ ,  $\beta n2 = 10$ ,  $\beta n5 = 0.5$ A or B = "1"A=B="`1"A = B = 0Vinv2

FIG. 27A FIG. 27B

Id of Tr1, (Tr2 + Tr5)

A = B = "1"

A or B = "1"

A = B = "0"

A = B = "0"

1

VR

VM

Vm

Vm

Vm Vout

 $\beta p1 = 10$ ,  $\beta n2 = 1$ ,  $\beta n5 = 0.5$ 

AND

۸ ک

XNOR FIG. 29B A or B = "1"A = B = 0Id of Tr1, (Tr2 + Tr5)۸S Id\_high

Vinv2

FIG. 30

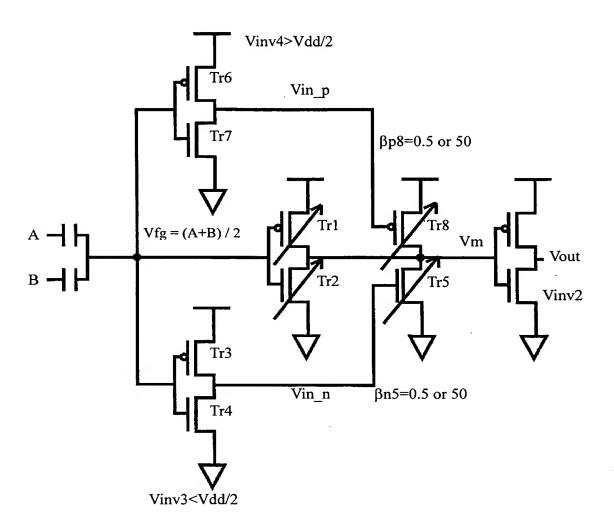
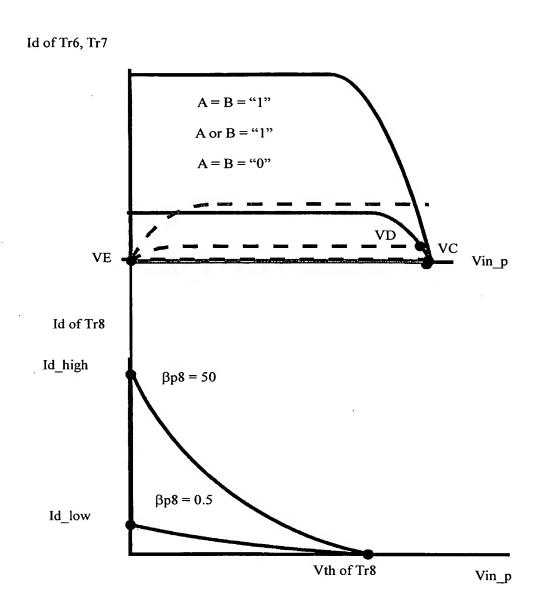


FIG. 31



Vm Vout "0" 00 ΛP VT XOR В 0 0 A 0  $\beta p1 = 1, \beta n2 = 10, \beta n5 = 0.5, \beta p8 = 50$ Vm A or B = "1"A=B=``1"A = B = 0Vinv2 Id of (Tr1 + Tr8), (Tr2 + Tr5) Id\_high

FIG. 32B

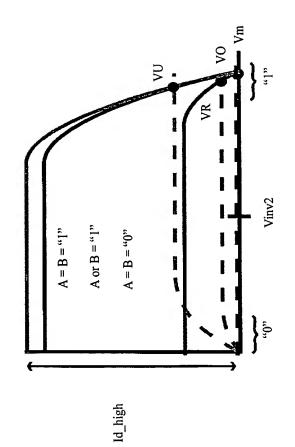
FIG. 32 A

FIG. 33B

**FIG. 33A** 

Id of (Tr1 + Tr8), (Tr2 + Tr5)

A B Vm Vout
0 0 VO "0"
0 1 VR "0"
1 1 VU "0"



 $\beta p1 = 10, \beta n2 = 1, \beta n5 = 0.5, \beta p8 = 50$ 

all "0"

Vm Vout

В

"0"

N

NAND

 $\beta p1 = 1, \beta n2 = 10, \beta n5 = 50, \beta p8 = 50$ 

Vm Vout ,,0,, > VS VR NOR FIG. 35B В  $\beta p1 = 10, \beta n2 = 1, \beta n5 = 50, \beta p8 = 50$ m ∧m Vinv2 A or B = "1"A=B=``0"Id of (Tr1 + Tr8), (Tr2 + Tr5) 1 FIG. 35A Id\_high

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A Q<sub>2</sub> (AD CONVERTER)

A Q<sub>2</sub> (AD CONVERTER)

Q<sub>4</sub> (AD CONVERTER)

FIG. 37

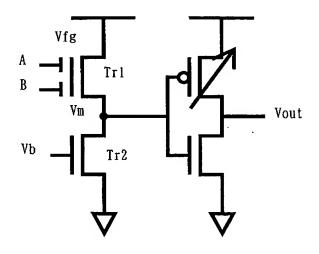


FIG. 38

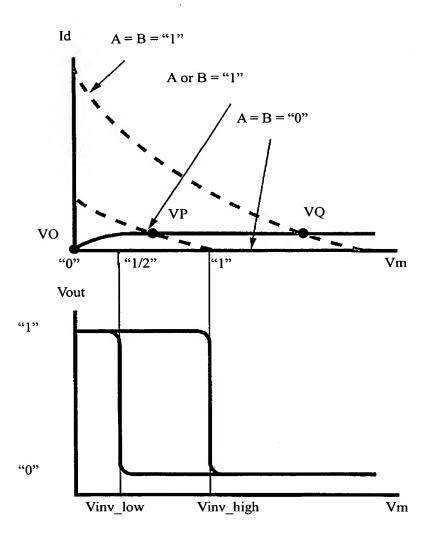


FIG. 39

 Vout

 A
 B
 Vm
 Vinv\_low
 Vinv\_high

 0
 0
 VO
 1
 1

 0
 1
 VP
 0
 1

 1
 1
 VQ
 0
 0

 NOR
 NAND

FIG. 40

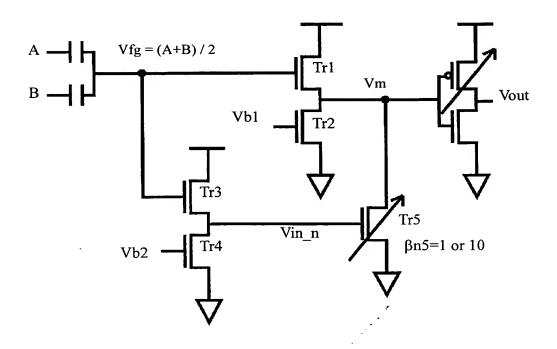
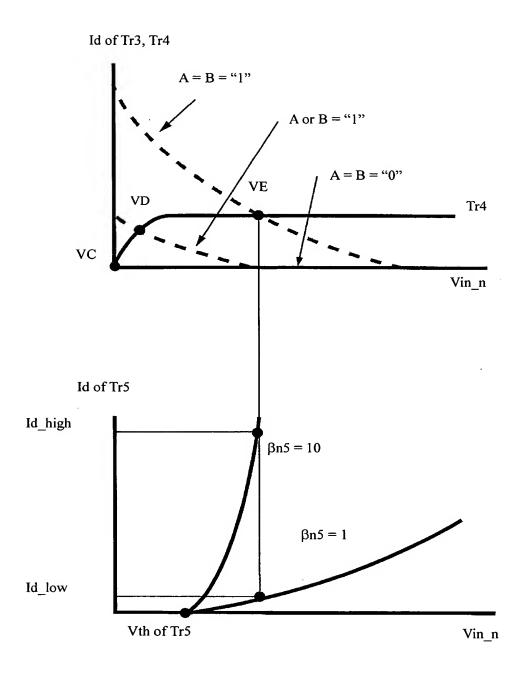
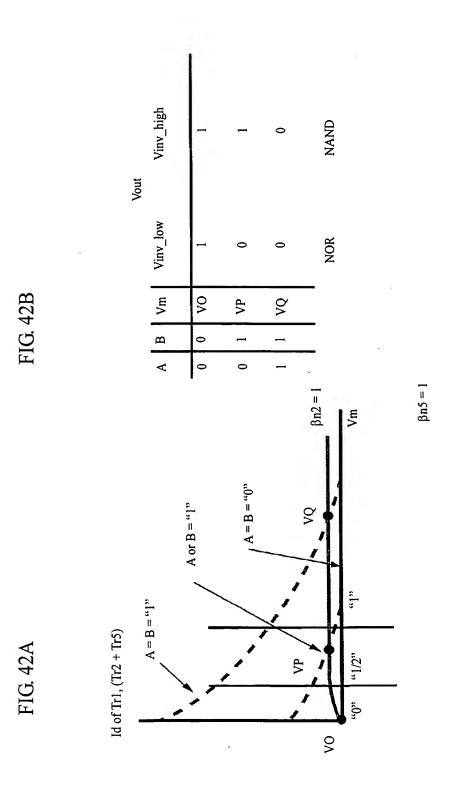
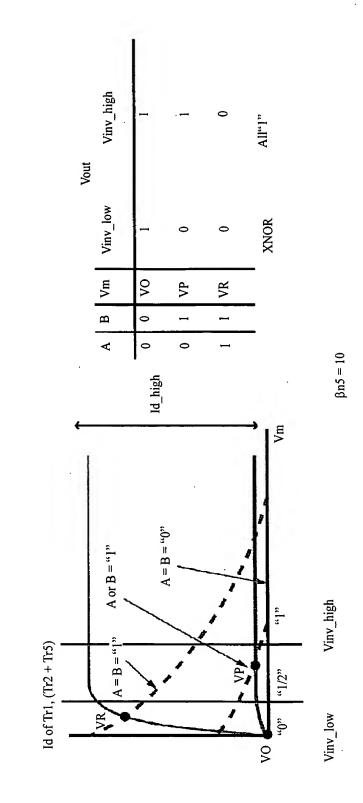


FIG. 41







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FIG. 44

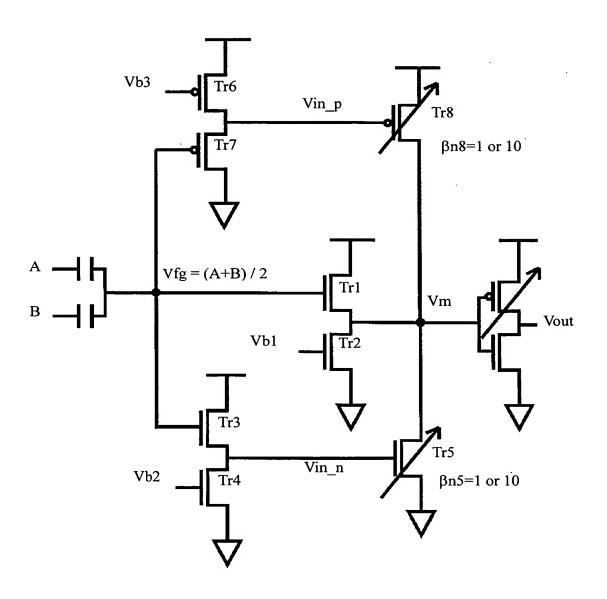
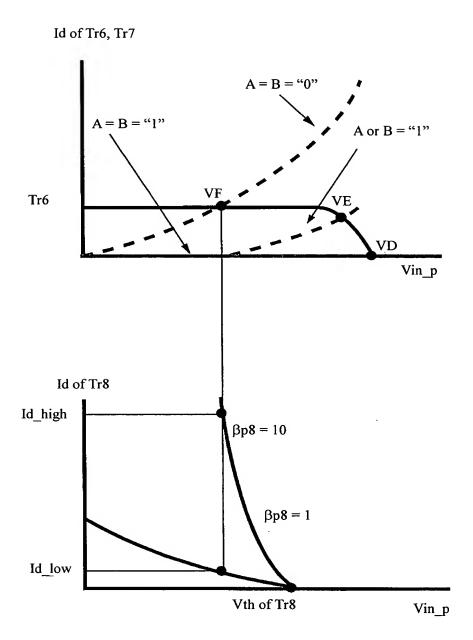


FIG. 45



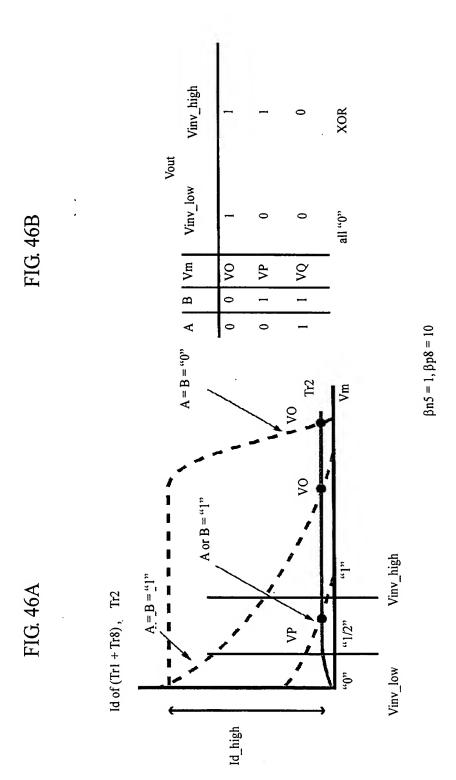


FIG. 47A

 A
 B
 Vm
 Vinv\_low
 Vinv\_high

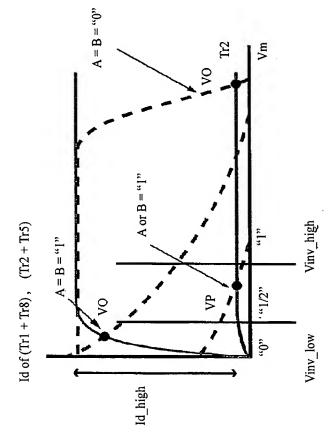
 0
 0
 VO
 1
 1

 0
 1
 VP
 0
 1

 1
 VP
 0
 1

 1
 VQ
 0
 0

 AND
 OR



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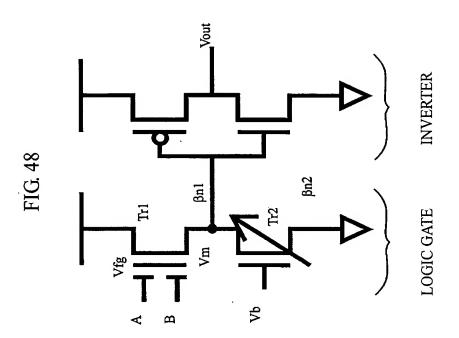


FIG. 49

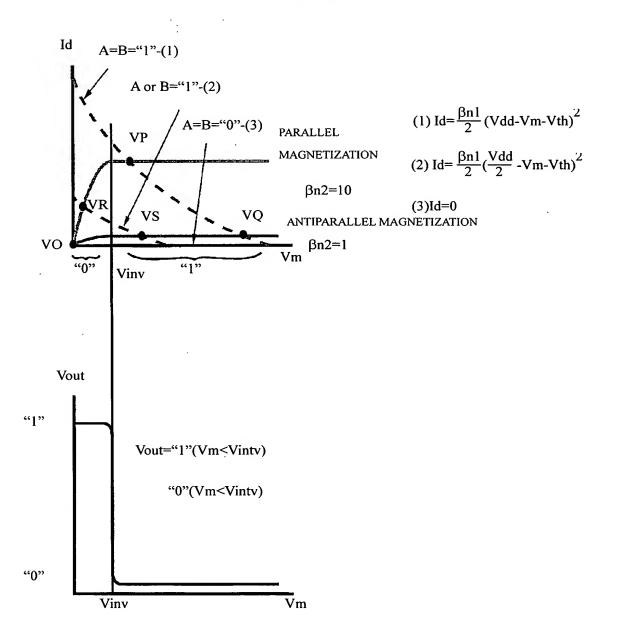


FIG. 50	$\beta n2 = 10$	Vout	1	1	0	
		Λm	OA	VR	VP	- NAND
		В	0	-	1	- Z
		A	0	0	1	_
		Vout	1	0		_
	$\beta$ n2 = 1	Vm.	ΛO	NS	VQ	NOR
	βug	В	0	1	-	
		4	0	0	-	

FIG. 51A

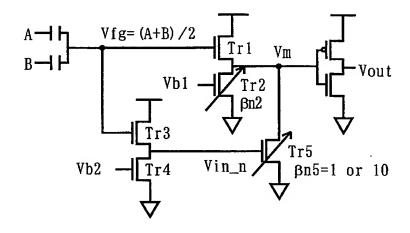
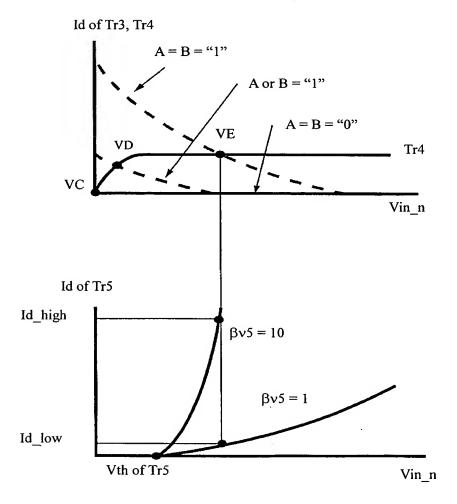


FIG. 51B



B Vm Vout  $\beta n2 = 10$ VR NAND VP A B Vm Vout NOR  $\beta n2 = 1$  $\beta n2 = 1$ ٥ A or B = "1"A = B = "1"Id of Tr1, (Tr2 – Tr5) 00

 $\beta m5 = 1$ ,  $\beta n2 = 1$  or 10

FIG. 53B

Vout	1	0		
Vm	0/	NS	VU	_
В	0	-	-1	
∢	0	0	-	_

XNOR

 $\beta p5 = 10, \beta n2 = 1$ 

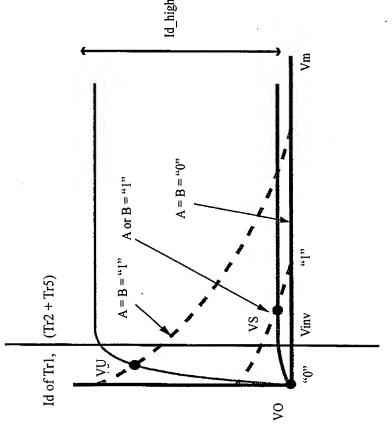
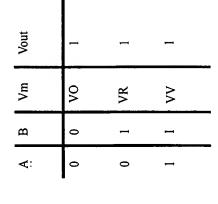
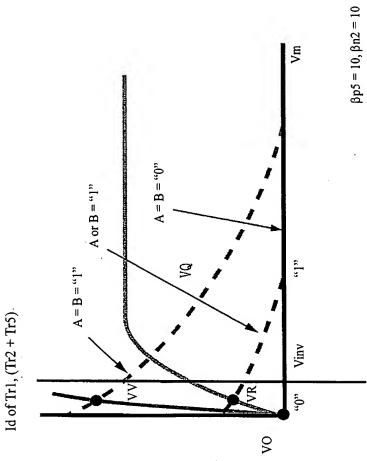


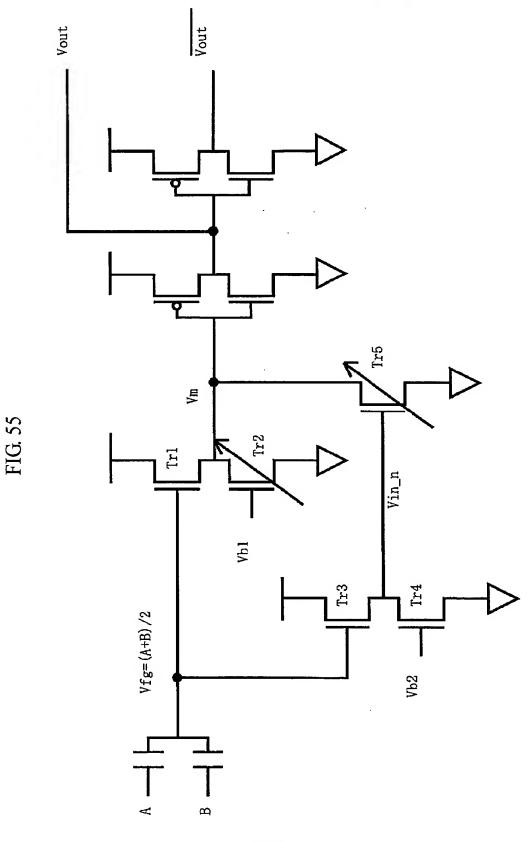
FIG. 53A

FIG. 54A



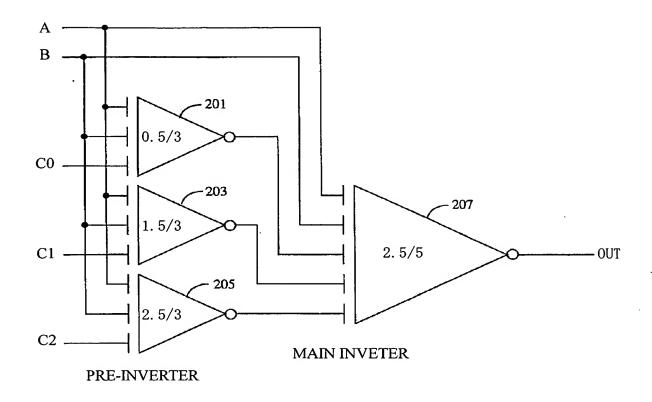
all "1"



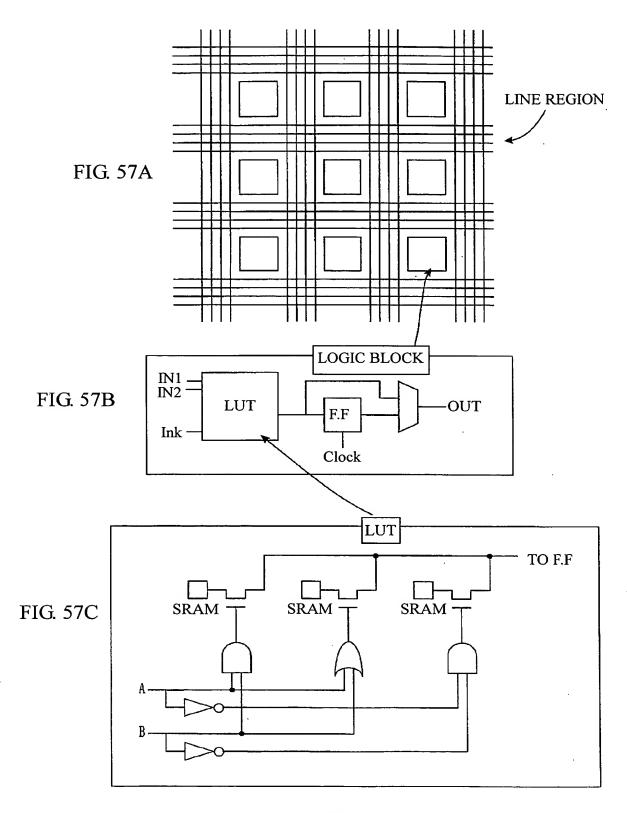


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FIG. 56



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